Applicant(s): Akita SHINICHI

Serial No.: 10/512,102

For: NOISE FILTER CIRCUIT

Filed: October 21, 2004

Examiner: Dinh Thanh LE

Art Unit: 2816

Confirmation No.: 6415

Customer No.: 27623 Attorney Docket: 0001864USU/3105

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

fee set forth in 37 C.F.R. §1.17(p);

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

In accordance with applicant's duty of disclosure under 37 C.F.R. §1.56, please find attached hereto form PTO-1449 listing information which may be material to the patentability of this application, filed concurrently herewith. This Information Disclosure Statement is being filed:

| | Within three (3) months of the filing date of the national application; |
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| | Within three (3) months of the date of entry of the national stage as set forth in 37 C.F.R. §1.491 in an international application; |
| | Before the mailing date of a first Office Action on the merits; |
| <u>xxx</u> | After the filing date or date of first Office Action, but before the mailing date of a final action under 37 C.F.R. §1.113, provided that this occurs prior to the issuance of a Notice of Allowance and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e) or the fee set forth in 37 C.F.R. §1.17(p); |
| | After the filing date or date of first Office Action, but before the mailing date of a Notice of |

Allowance under 37 C.F.R. §1.311, provided that this occurs prior to the final action and provided that this I.D.S. is accompanied by either a certification as specified in 37 C.F.R. §1.97(e) or the

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| After the mailing date of a final action under 37 C.F.R. §1.113, provided that this occurs prior to the issuance of a Notice of Allowance and provided that this I.D.S. is accompanied by a certification as specified in 37 C.F.R. §1.97(e) and the fee set forth in 37 C.F.R. §1.17(p); and |
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| After the mailing date of a Notice of Allowance under 37 C.F.R. §1.311, provided that this occurs prior to or subsequent to the payment of the Issue Fee and provided that this I.D.S. is accompanied by a certification as specified in 37 C.F.R. §1.97(e) and the fee set forth in 37 C.F.R. §1.17(p). |
| Filng with RCE Under 37 CFR 1.114, thus no fee is required. |
| |

A copy of the the aforementioned article is enclosed herewith.

It should be understood that attention has been called to the references that have been deemed to be pertinent to the claimed present invention. In concluding what was pertinent, the criteria employed was considered most appropriate in light of the invention shown in the present application. However, the Examiner or others may deem some other criteria to be just as appropriate or more appropriate. Therefore, the Examiner is respectfully urged to review the listed references and to make the usual careful independent search for other prior art that may be pertinent.

Respectfully submitted,

September 11, 2006

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| | OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | | | |
| | Johns et al., ANALOG INTEGRATED CIRCUIT DESIGN, John Wiley & Sons, Inc., New Yor | | | | | | | | | |
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP §609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.

DATE CONSIDERED

EXAMINER